

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF THE CLAIMS:

1. (Currently Amended) A microcomputer, comprising:  
a first timer having a first resolution that can be used for controlling cooling equipment; and  
a second timer that has a second resolution higher than said first resolution and that can be used for adjusting ~~the~~ brightness of a display device,  
wherein said first timer and said second timer are formed on a semiconductor integrated circuit,  
wherein said first timer outputs a first signal of a first frequency,  
wherein said second timer outputs a second signal of a second frequency higher than said first frequency,  
wherein said second timer is operable to control charging and discharging of a battery coupled to said semiconductor integrated circuit, and  
wherein said signal of said first frequency and said signal of said second frequency are generated by frequency-dividing a standard clock signal.

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2-4. (Cancelled)

5. (Original) The microcomputer according to claim 1, wherein said second timer comprises a pulse width conversion circuit.

6. (Currently Amended) The microcomputer according to claim 21,

wherein said first timer ~~can~~ is operable to output one of a plurality of signals ~~of~~ as said signal of said first frequency,

wherein said second timer outputs a third signal of a third frequency for controlling the battery, and

wherein said second timer can output a plurality of signals that is the signal of the second frequency higher than said first frequency and signal differs in a pulse width from said third signal.

7. (New) The microcomputer according to claim 1, further comprising:

a keyboard controller for controlling a keyboard coupled to said semiconductor integrated circuit.

8. (New) A microcomputer formed on a semiconductor integrated circuit, comprising:

a first pulse unit having a first resolution, operable to control a cooling device; and

a second pulse unit having a second resolution higher than said first resolution and operable to adjust brightness of a display device;

wherein said second pulse unit is operable to control charging and discharging of a battery device;

wherein said first pulse unit is operable to output a first pulse signal of a first frequency;

wherein said second pulse unit is operable to output a second pulse signal of a second frequency and a third pulse signal of a third frequency; and

wherein said first frequency and said second frequency are obtained by frequency-dividing a standard clock signal.

9. (New) The microcomputer formed on a semiconductor integrated circuit according to Claim 8,

wherein said second and third frequencies are higher than said first frequency, and

wherein said third pulse signal is a control signal for charging and discharging said battery device.